

METHOD OF OVERTONE SELECTION AND LEVEL CONTROL
IN AN INTEGRATED CIRCUIT CMOS NEGATIVE RESISTANCE
OSCILLATOR TO ACHIEVE LOW JITTER

5 ABSTRACT OF THE DISCLOSURE

A stable high-frequency clock pulse apparatus and method including frequency source, overtone crystal unit coupled with a negative resistance oscillator, a bandpass overtone filter and a drive level control is provided. The apparatus can include a
10 frequency multiplier. The frequency source can be a micromechanical resonator.

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